

Fig. 1

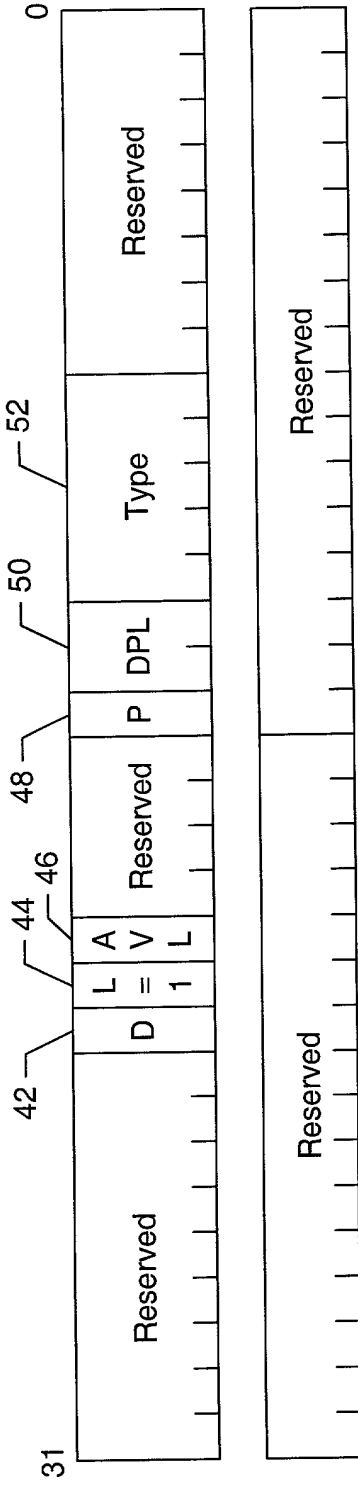


Fig. 2

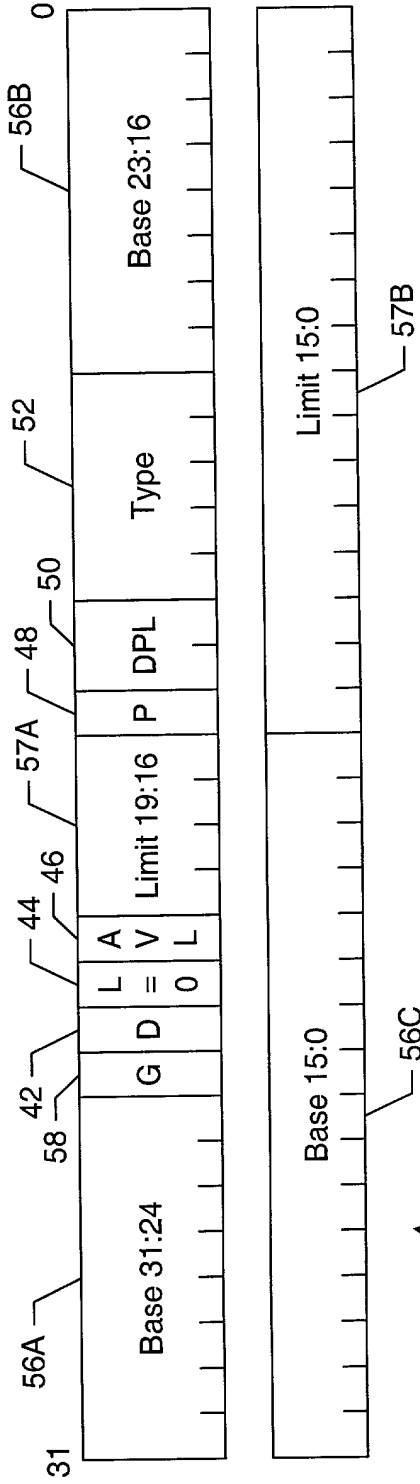


Fig. 3

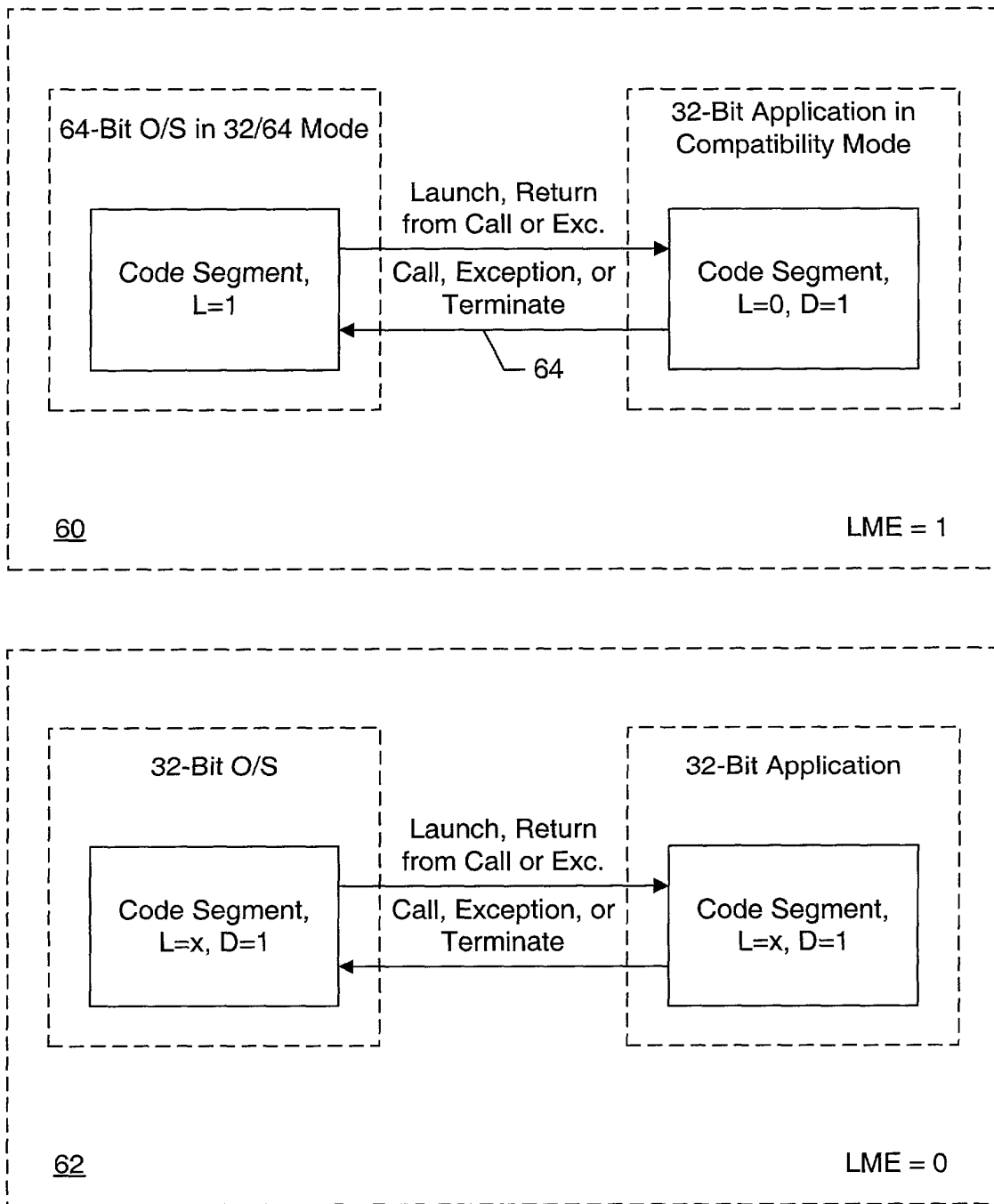


Fig. 4

<u>LME</u>	<u>CS L Bit</u>	<u>CS D Bit</u>	<u>Operating Mode</u>
0	x	0	16 Bit Mode
0	x	1	32 Bit Mode
1	0	0	16 Bit Compatibility Mode
1	0	1	32 Bit Compatibility Mode
1	1	0	32/64 Mode
1	1	1	Reserved

70  Fig. 5

Number of Override Prefixes (66H Operand, 67H Address)		0	1	2+
32/64 Mode	Operand Size	32 bits	64 bits	16 bits
	Address Size	64 bits	32 bits	32 bits
32 Bit Mode Incl. Compatibility Mode	Operand Size	32 bits	16 bits	16 bits
	Address Size	32 bits	16 bits	16 bits
16 Bit Mode Incl. Compatibility Mode	Operand Size	16 bits	32 bits	32 bits
	Address Size	16 bits	32 bits	32 bits

72  Fig. 6

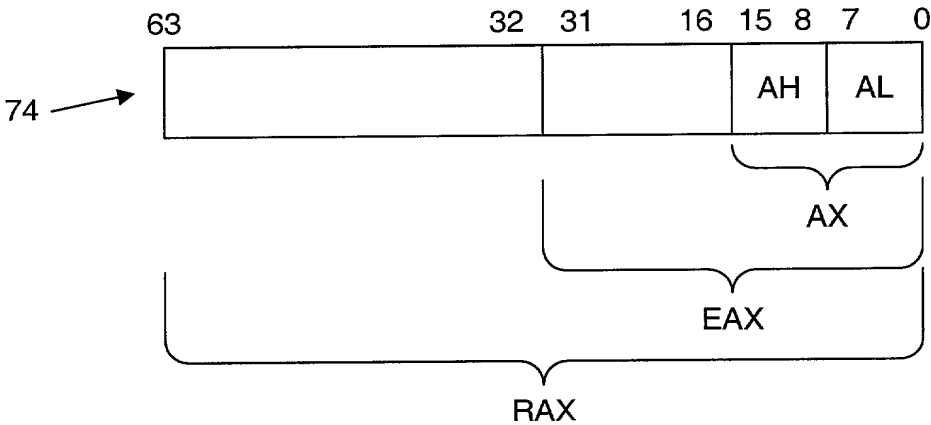


Fig. 7

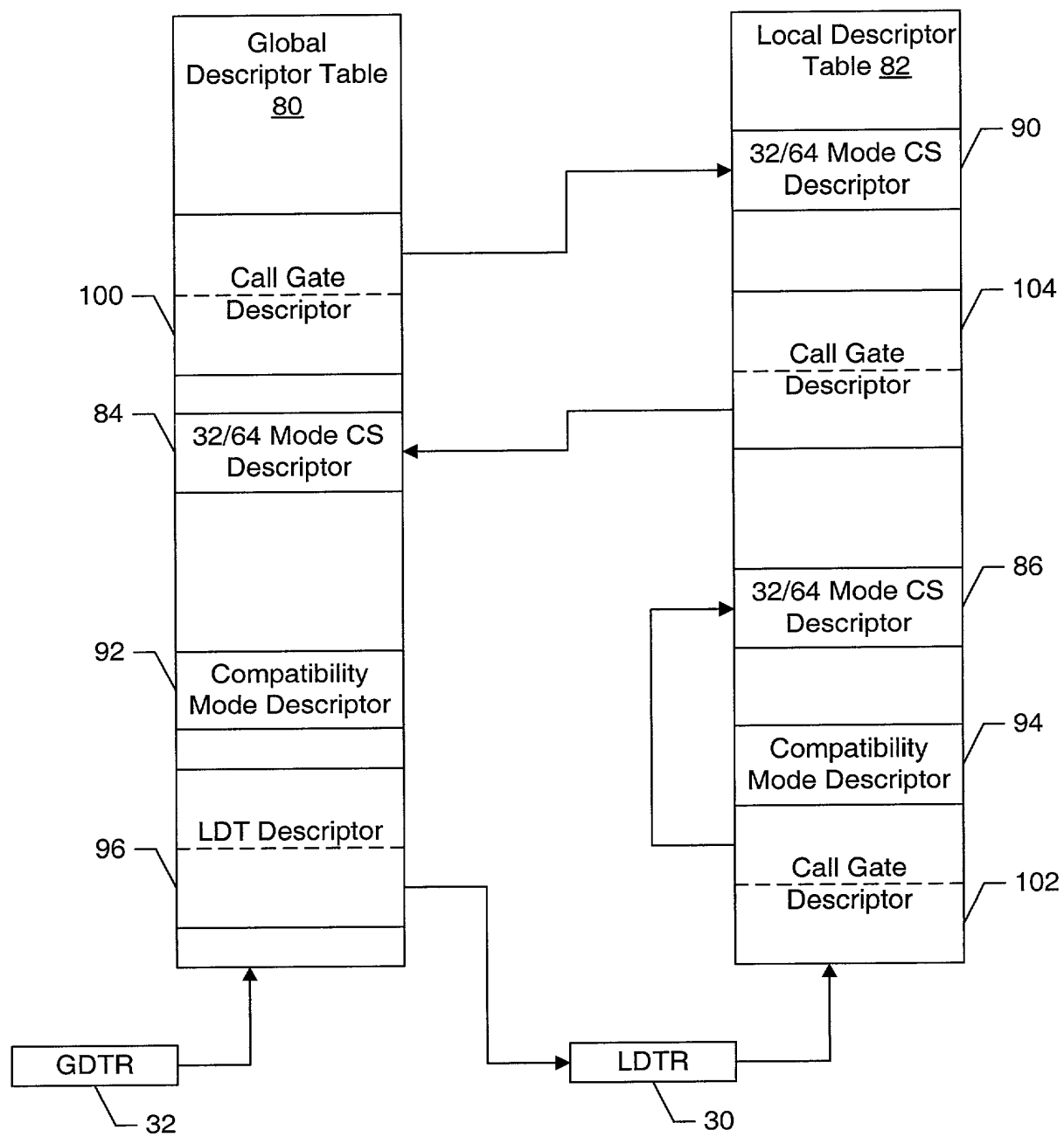


Fig. 8

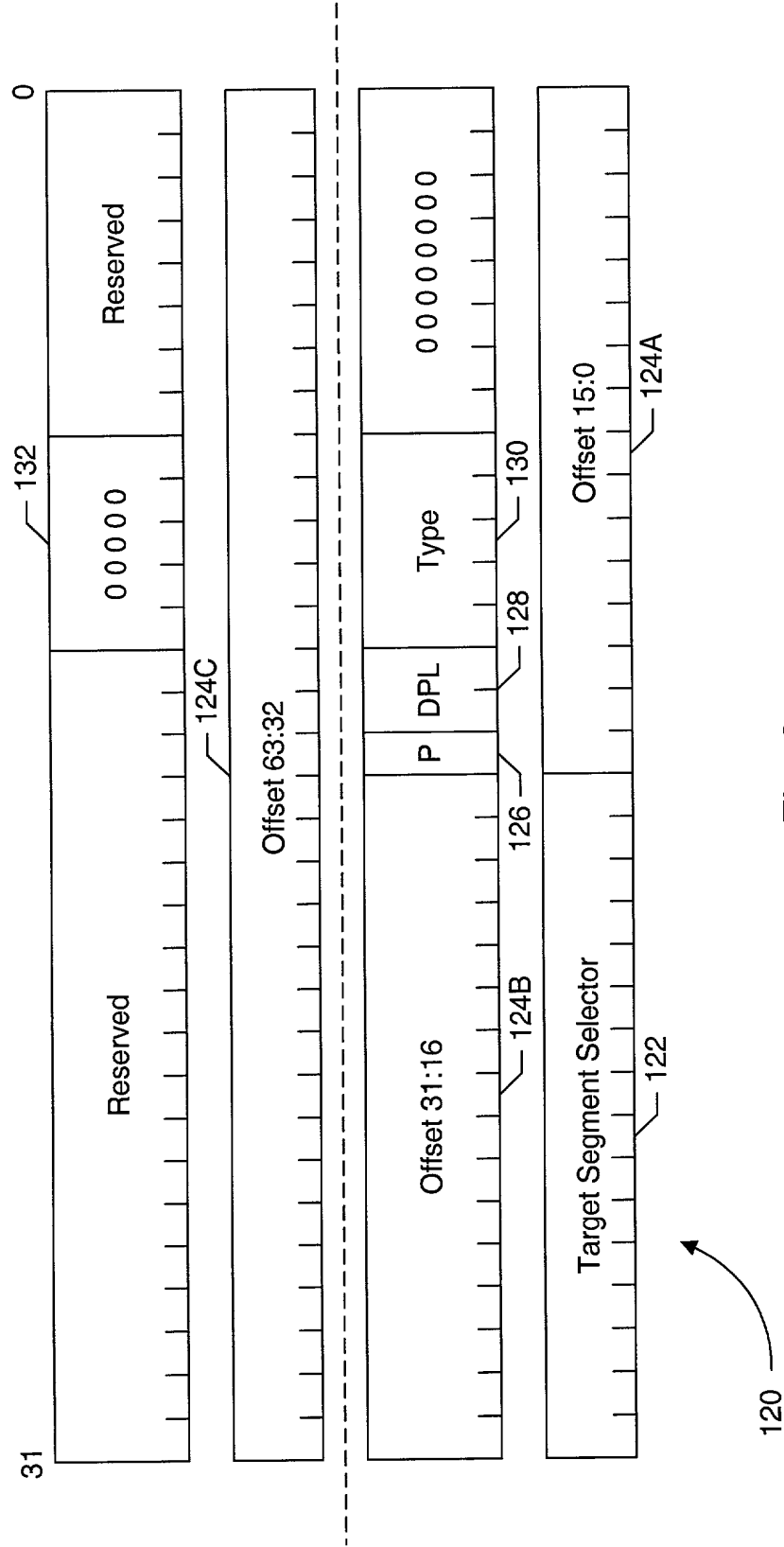


Fig. 9

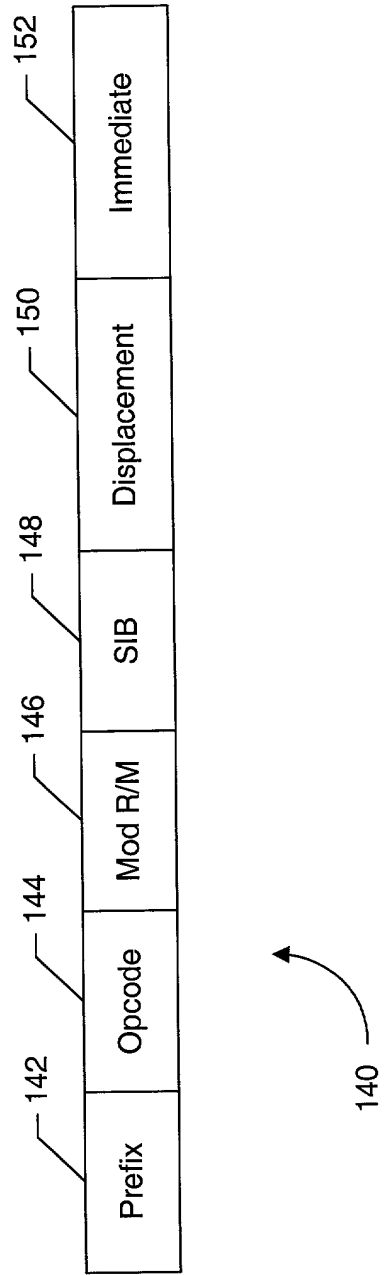


Fig. 10

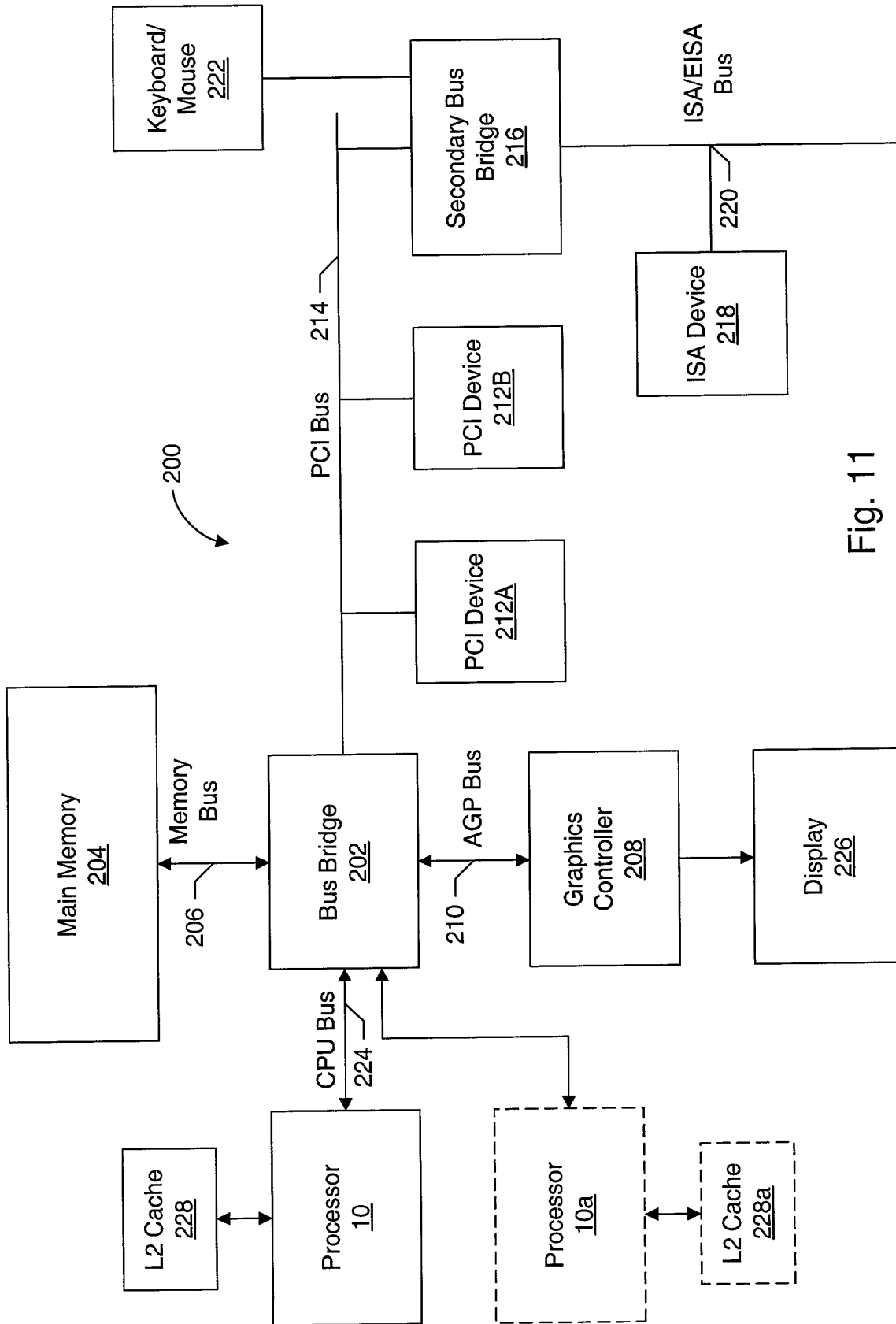


Fig. 11

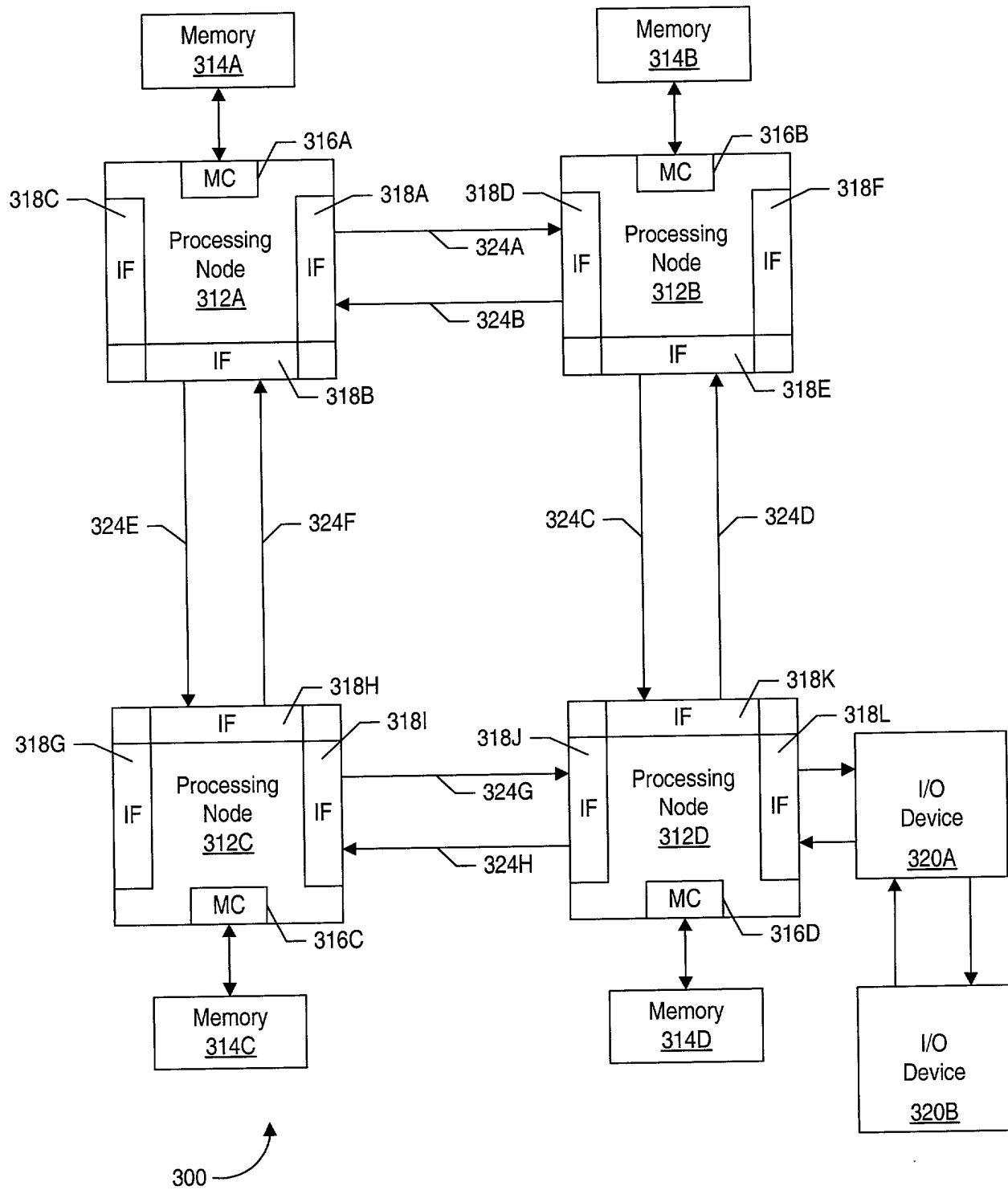


Fig. 12

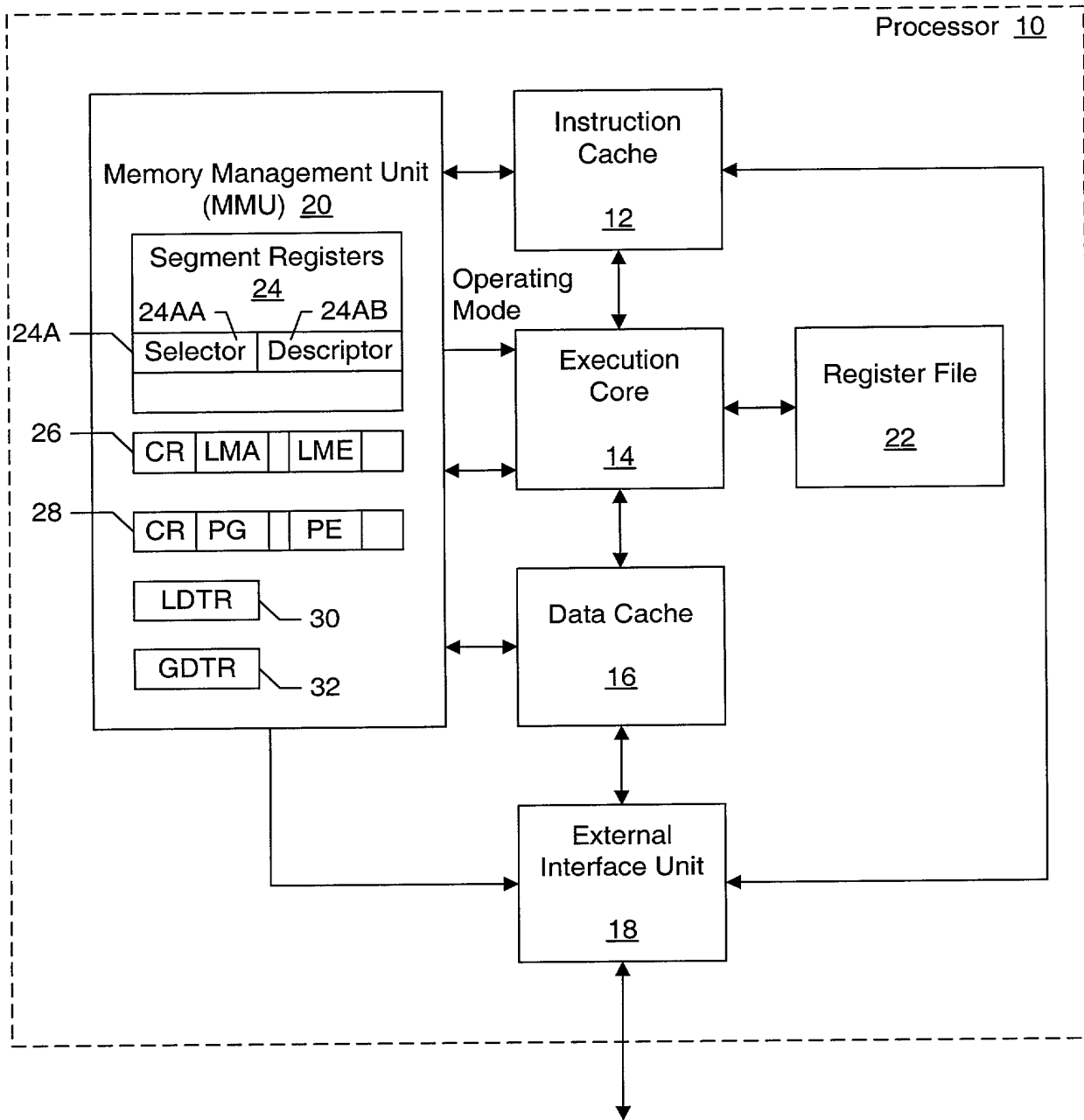


Fig. 13

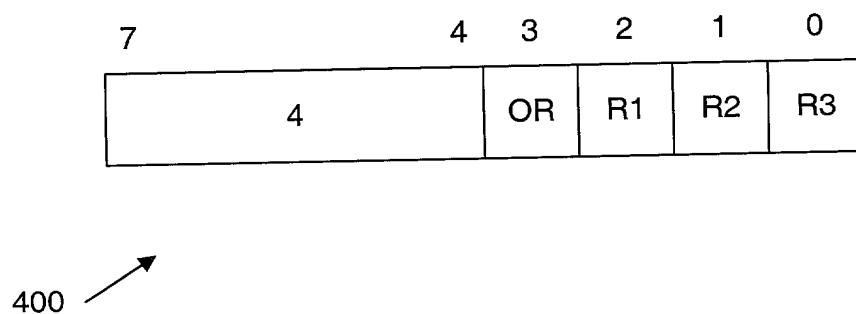


Fig. 14

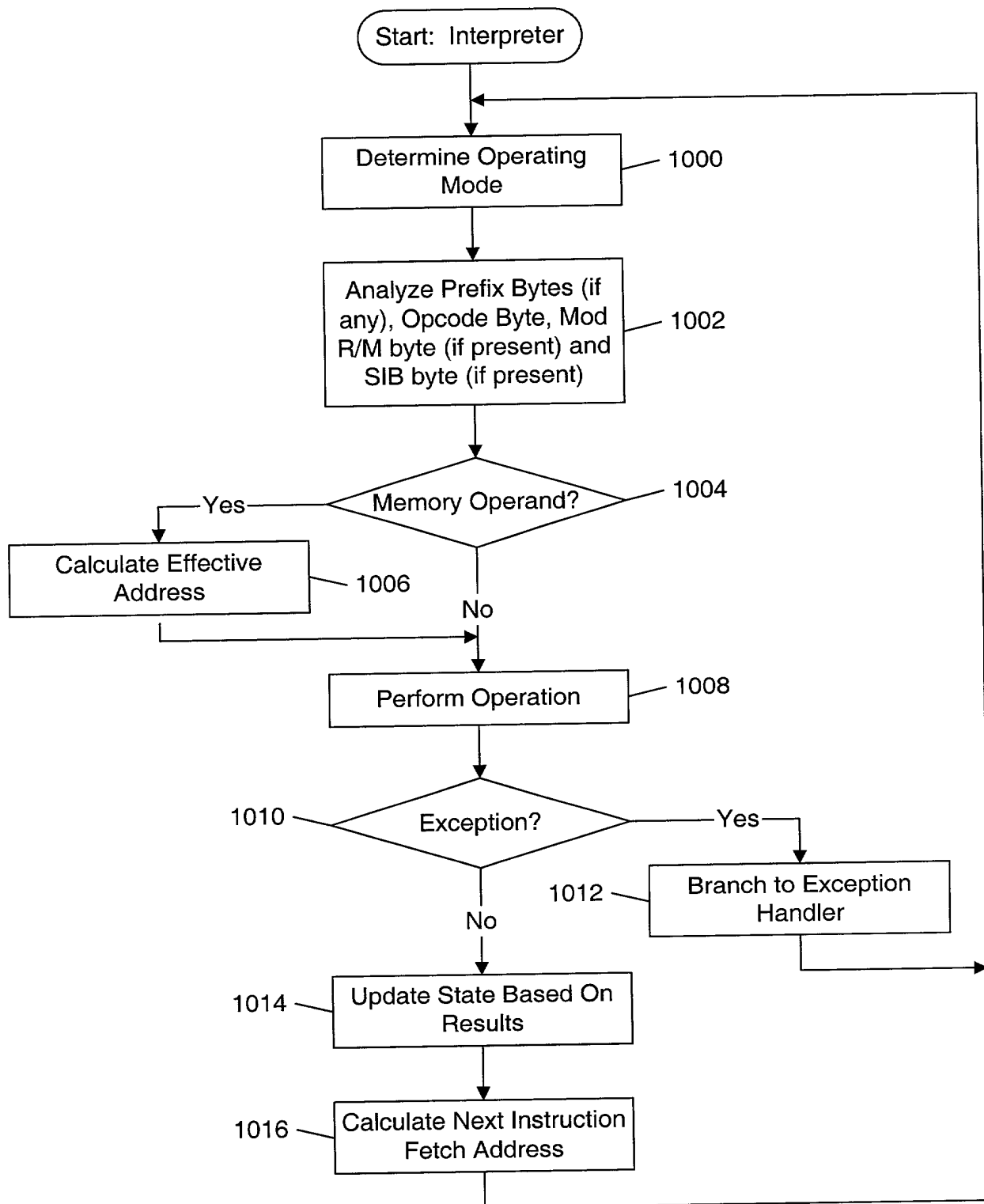


Fig. 15

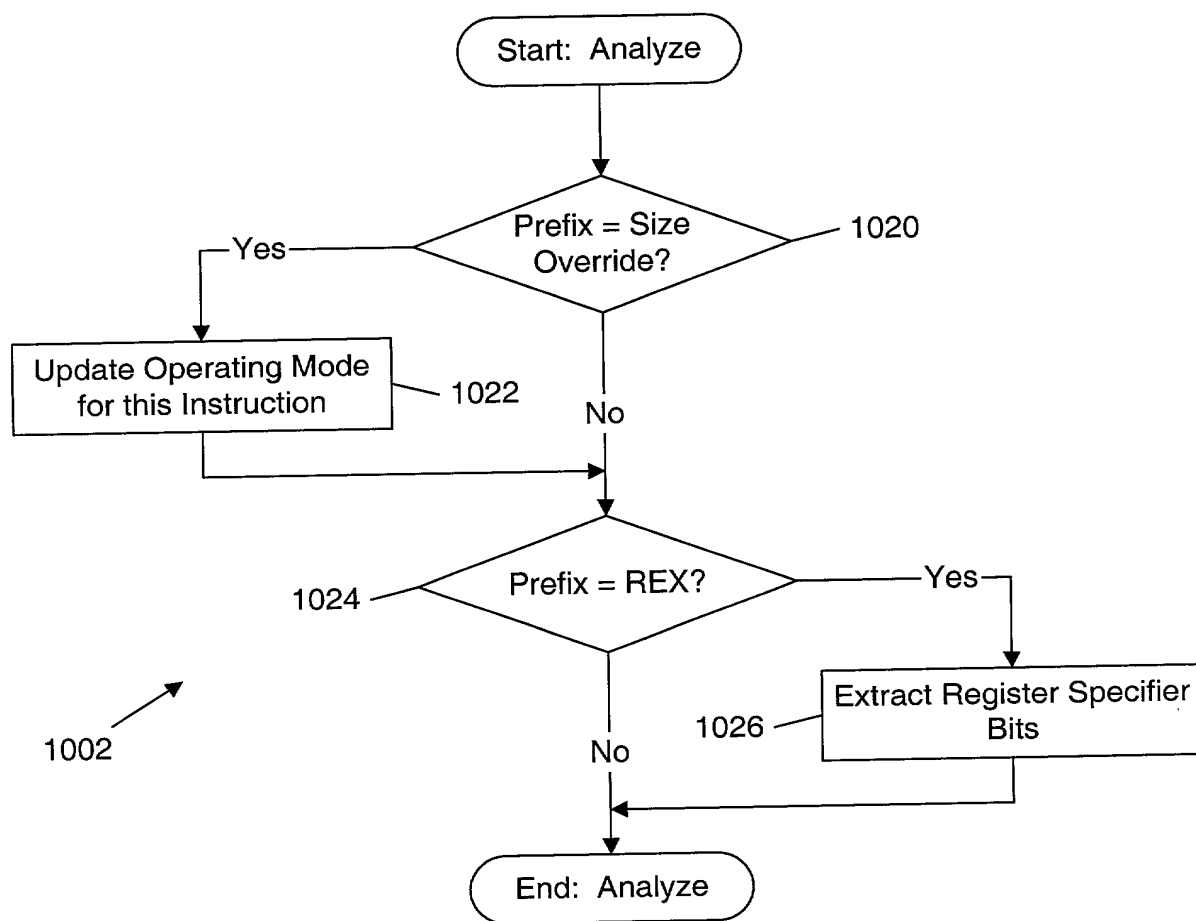


Fig. 16

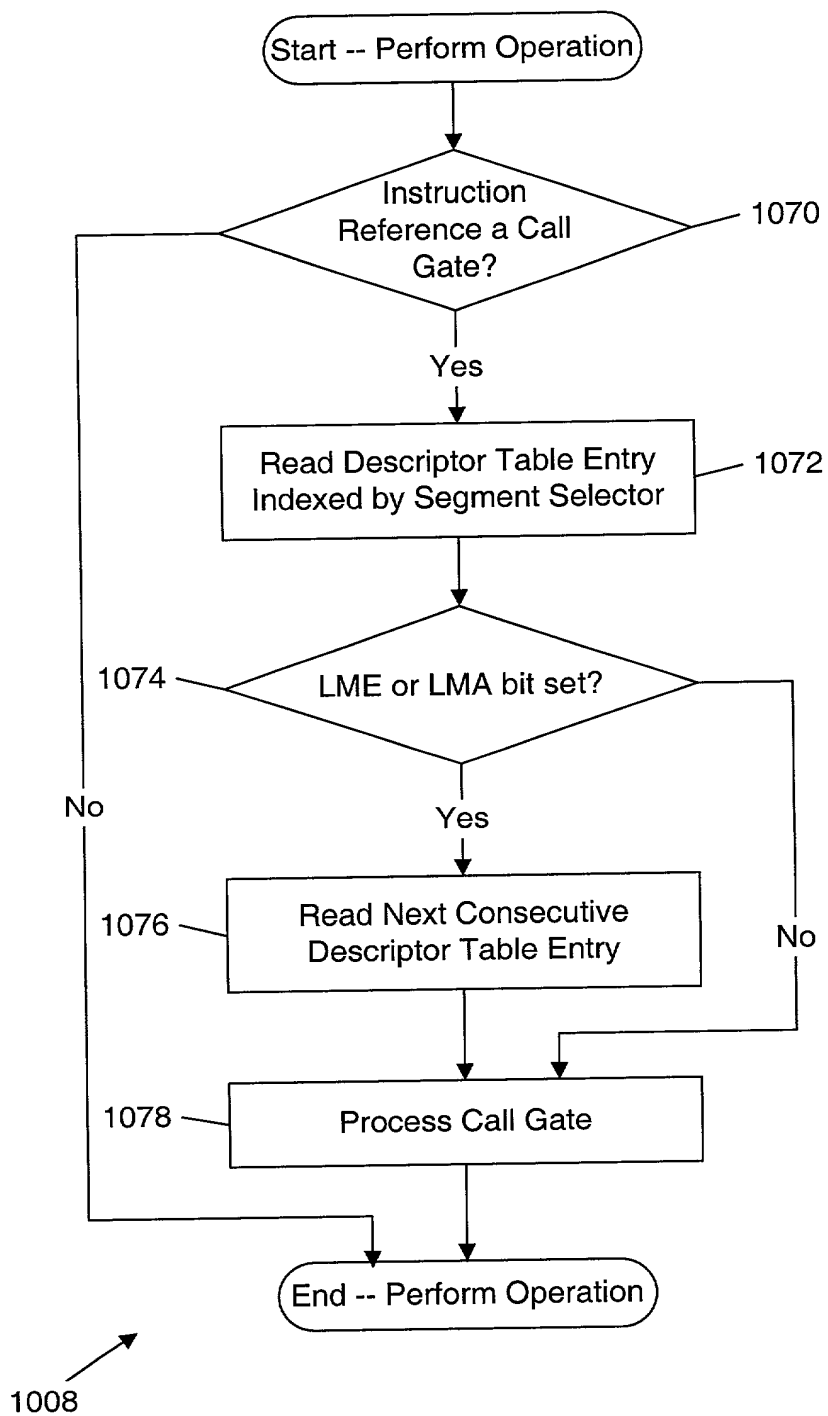


Fig. 17

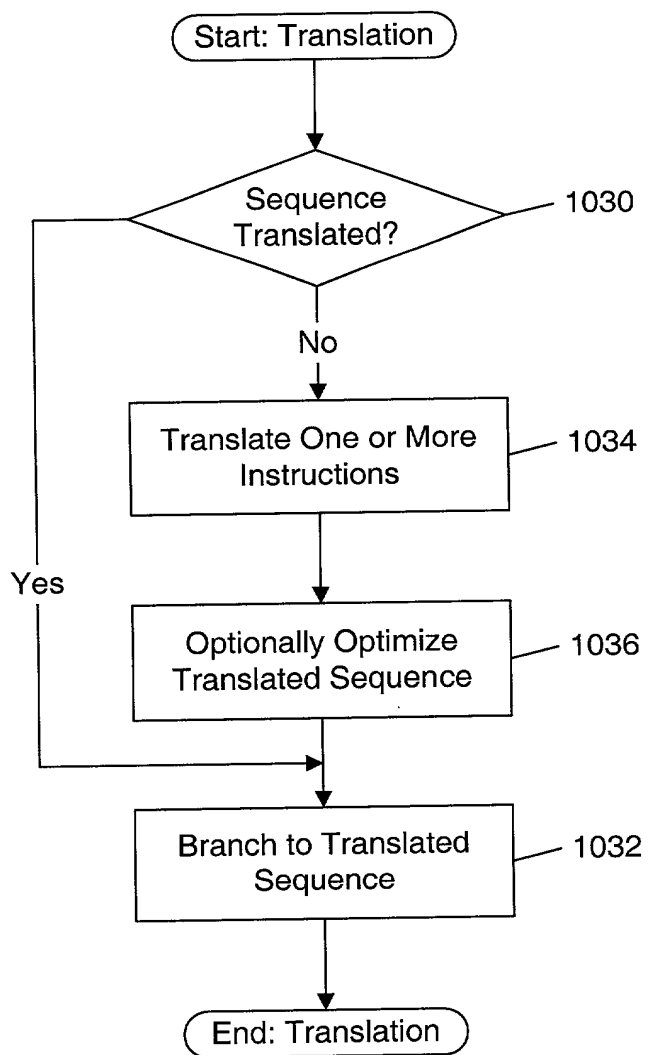


Fig. 18

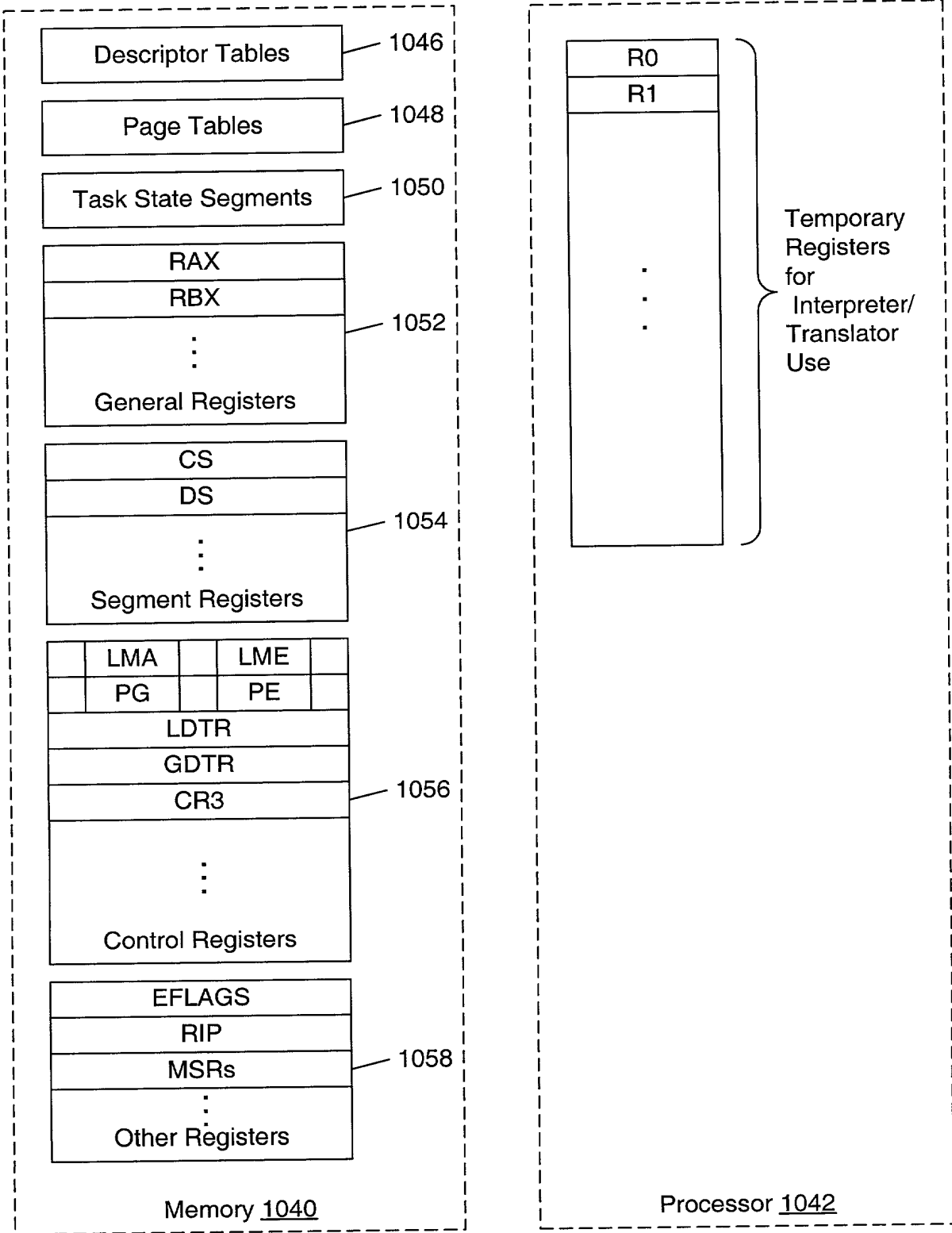


Fig. 19

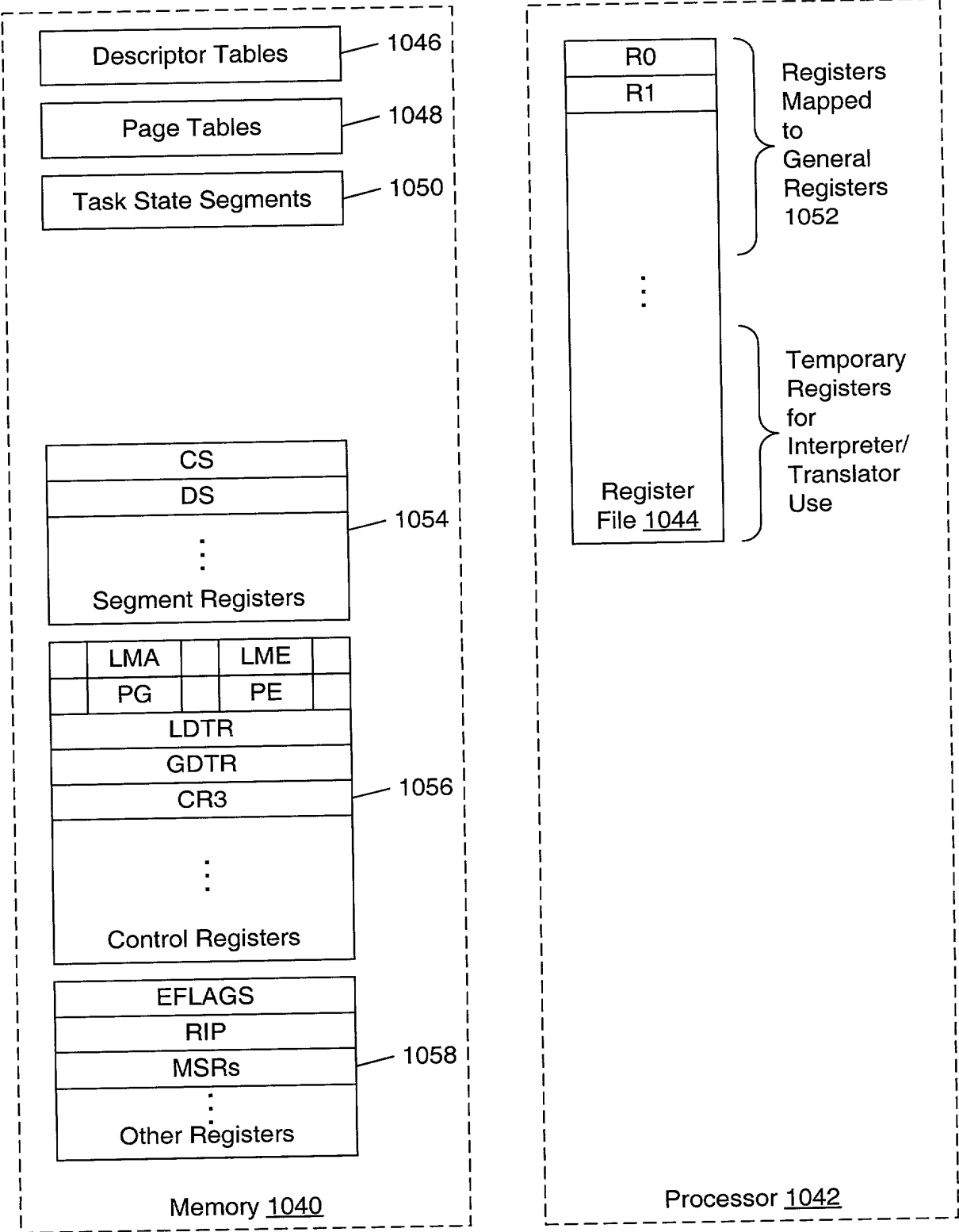


Fig. 20

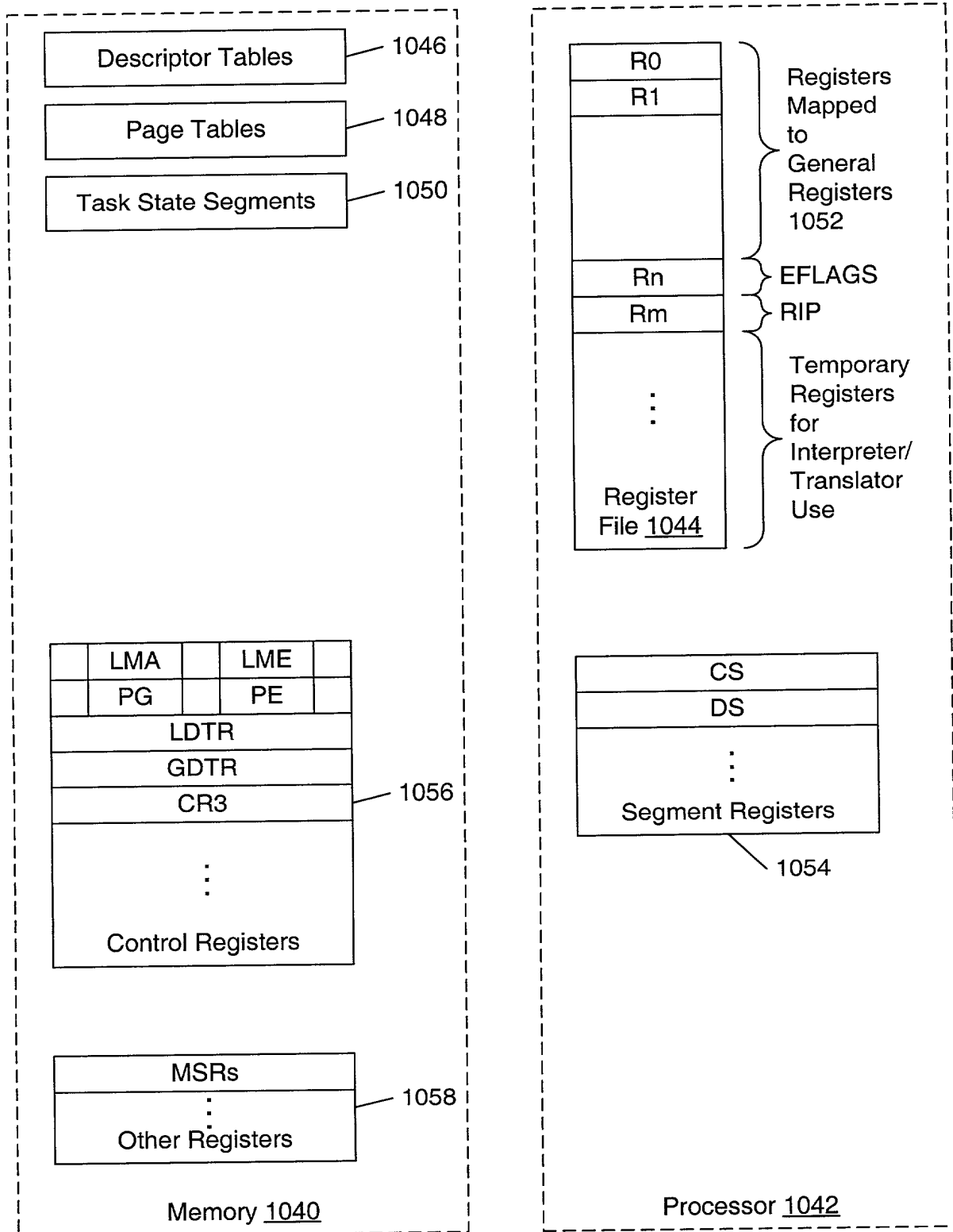


Fig. 21

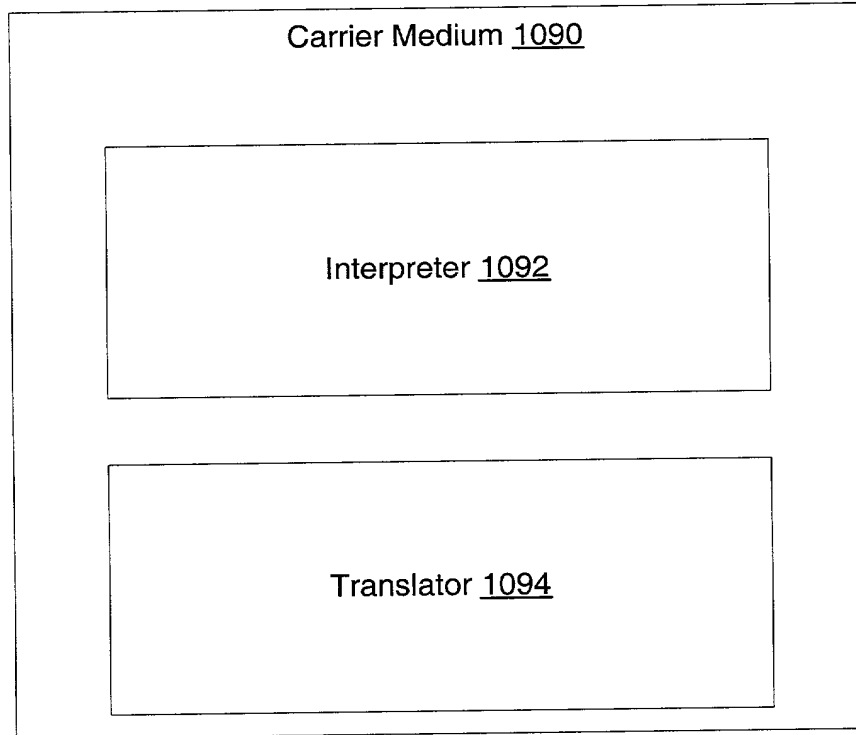


Fig. 22